

PROFILE

I have spent 25 years with development for electronics manufacturing, hardware test and verification. Developing the things that makes manufacturing to work and the things that makes hardware engineering to be efficient is what I love.

It is the boundary where hardware and software meet, where people and technology intersect that thrills me. I like when things are moving fast, to push forward and get results. I like to innovate and build something new. I absolutely love to fix hard problems and got a good eye for spotting things that can be improved.

Others describe me as a very processoriented, self-motivated person with high work capacity and strong attention to details. I have consistently received top-rated performance grades throughout my career.

CONTACT

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ABOUT E-SHARP AB

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DANIEL RHODIN



CONSULTANT

WORK EXPERIENCE

Senior Architect/R&D Manager, Hardware Test Engineering [Synopsys] 2018 - Present

- Architected and implemented the 'Host Controller Interface' bus, used for high reliability communication over embedded programmable logic (FPGA/CPLD)
- Implemented Excel dashboards for statistics using VSTO.NET
- Developed drivers and hardware for test interface, measurement equipment and test fixture control
- C# .NET expert and architect for all core components within the department
- Introduced Jira and dashboards to improve visibility/efficiency
- Technical project management

Senior Architect/Manager, Industrialization/NPI [Synopsys]

2012 - 2018

Architected and implemented the complete test framework and manufacturing execution system (MES) with a MySQL backend

Senior Staff Engineer [ST Ericsson]

2007 - 2012

- Development of test automation framework
- Invented a method for shunt-less battery capacity estimation (Pat. <u>US20120323512</u>)

Senior Staff Engineer [Sony Ericsson]

2002 - 2007

Designed test interface product for manufacturing, including electronics, firmware, software

Earlier employments

2000 – 2002 Business development manager [Acterna] 1998 – 2000 Technical Project Manager [Ericsson] 1996 – 1998 Verification Engineer [Ericsson]

EDUCATION

Mittuniversitetet

B.Sc Software Engineering

SKILLS

C# Electronics design C/C++ Project Management MySQL Embedded systems System architecture Linux Machine Learning Git Jira WCDMA/GSM/WLAN ActiveX/COM JTAG/Boundary Scan